

**IN THE SPECIFICATION:**

Page 3, lines 4 to 10, replace the paragraph with the following amended paragraph.

Fig. 1 shows a front view of a part of a PCB layer. The leads (line segments) marked in black 1a, 2b, 1c, 2d are on the front side of the layer, and the leads marked in grey 2a, 1b, 2c, 1d are on the back side. A lead 1a on the front side passes along a micro via 3a to the back side, on the back side in the path 1b, and back to the front side through the layer in micro via 3b, and on the front side 1c. The other lead 2a passes on the back side to micro via 3d and to the front side, along the front side 2b to via 3e, on the back side 2c to another micro via 3f and back to the front side 2d. It can be seen that each lead is tapered in cross section from opposite ends thereof to a middle area, and that middle areas of corresponding leads on opposite sides of the layer cross one another.